

AMENDMENTS TO THE CLAIMS

The listing of claims presented below will replace all prior versions and listings of claims in the application.

Listing of claims:

1. **(Currently Amended)** A data write-in method for a flash memory, wherein the flash memory comprises at least two flash chips and a controller, and the method comprises:

partitioning physical blocks in the **at least** two flash chips such that the physical blocks in one of the **at least two flash** chips have odd logical block addresses and the physical blocks in ~~the other another one~~ of the **at least two flash** chips have even logical block addresses;

the controller receiving a data write-in instruction and analyzing a beginning logical address for writing from the received data write-in instruction;

the controller obtaining the logical block address needed to be written according to the analyzed beginning logical address;

the controller determining a parity of the obtained logical block address, and selecting one flash chip from the flash chips according to the determined parity of the logical block address;

the controller directing a first programming or erasing instruction to the physical blocks corresponding to the obtained logical block address in the selected flash chip;

the controller detecting whether ~~the other~~ **said another one** flash chip needs to be programmed or erased while the first programming or erasing instruction are being processed; if programming or erasing is needed in ~~the other~~ **said another one** flash chip, the method further comprises:

the controller directing a second programming or erasing instruction to ~~the other~~
said another one flash chip of at least two flash chips.

2. (Cancelled)

3. (Previously Amended) The data write-in method for a flash memory according to claim 1, wherein if the other flash chip does not need to be programmed or erased, the method further comprises:

judging whether the processing of the first programming or erasing instruction is finished.

4-5. (Cancelled)

6. (Previously Amended) The data write-in method for a flash memory according to claim 1, wherein the analyzing further comprises:

obtaining the number of sectors needed to be written from the data write-in operation instruction.

7. (Previously Amended) The data write-in method for a flash memory according to claim 6, the analyzing further comprises:

judging whether the data write-in instruction has been finished by subtracting a number of written sectors from a number of sectors needed to be written.